IBM Assembler Changes for MVS

May 30, 1997

The IBM High Level Assembler for MVS & VM & VSE is now the only supported assembler for MVS at NERDC. We encourage you to convert all of your MVS assembler JCL to use the new High Level Assembler now. Suggestions for using and converting to the new assembler are below.

Because IBM is no longer distributing Assembler XF, NERDC will be removing this assembler from the system on June 26, 1997. At that time, related JCL PROCs will also be deleted from the system (see below).

Two other assembler products will remain in the system for now but will be scheduled for removal at a future date. They are the IBM Assembler H Version 2 and the SLAC (Stanford Linear Accelerator Center) enhanced version of IBM Assembler H. Support for IBM Assembler H Version 2 ended on October 31, 1995.

Using the New IBM High Level Assembler for MVS & VM & VSE

The IBM High Level Assembler for MVS & VM & VSE is a new assembler from IBM that replaces Assembler H Version 2 and incorporates many of the SLAC enhancements. Most assembler source should assemble with no modification required. Some of the assembler options have changed, so minor option changes may be required. These options are documented in the IBM High Level Assembler for MVS & VM & VSE Programmer's Guide manual. The IBM High Level Assembler for MVS & VM manual bookshelf is currently available at URL:

http://Ppdbooks.pok.ibm.com:80/cgi-bin/bookmgr/bookmgr.cmd/Shelves/ASMSH003

The document, "IBM High Level Assembler Release 2: new features and functions", containing information about the new functions and options is available at the URL:


The following NERDC modified JCL PROCs, with the ASMHA* prefix, may be used to execute the new IBM High Level Assembler program (ASMA90) on MVS.
Recognizing Programs Using Unsupported Assemblers

**IBM Assembler XF**

As noted above, IBM Assembler XF is no longer being distributed or supported and NERDC will be removing it from the system on June 26, 1997. Use of this assembler is indicated by JCL executing PROCs with ASMF* or ASMX* prefixes or execution of any of the programs, ASMBLR, IEUASM, or IF0X00. You should convert any jobs referencing these PROCs or programs to the new IBM High Level Assembler for MVS & VM & VSE before June 26, 1997. On that date, Assembler XF and the following JCL PROCs will be deleted from the system:

ASMFC  ASMFE  ASMXC  ASMXE
ASMFCG  ASMFG  ASMXCG  ASMXG
ASMFCL  ASMFL  ASMXCL  ASMXL
ASMFCLE  ASMFL  ASMXCLE  ASMXLE
ASMFCLG  ASMXCLG  ASMXLIB

**IBM Assembler H Version 2**

Use of the IBM Assembler H Version 2 assembler is indicated by JCL executing PROCs with ASMH* prefixes or execution of the program IEV90 with no //STEPLIB DD statement.

**IBM Assembler H Version 2 with SLAC Enhancements**

Use of this assembler is indicated by JCL executing PROCs with ASMHX* prefixes or execution of the program IEV90 and the following statement:

```
//STEPLIB DD DSN=NERDC.SLAC.ASMH,DISP=SHR
```

If you have any questions about using MVS assemblers at NERDC, please call the UF Computing Help Desk at 352/392-HELP, e-mail ufhelp@cns.ufl.edu, or the NERDC Support Desk at 352/392-2061, e-mail consult.cns.ufl.edu.
Related Documents and Subjects

Related search topics include documentation, JCL, abends, and services.

News articles, NERDC documents, and many vendor manuals can be viewed online through DOCSYS, NERDC's documentation system. DOCSYS is available through NERDOC and the NERDC Interactive Services Menu (both from VTAM) as a free service, or from your CMS or TSO session. You can also access DOCSYS from the World Wide Web by opening the URL "http://www.cns.ufl.edu/". Enter HELP DOCSYS on CMS or TSO for more information about accessing DOCSYS.

Your Comments are Welcome

We welcome your comments and suggestions on this and all UFIT documentation. Please send your comments to:

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